

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

	Jong Shik Yoon, et al.	Docket No.:	TI-37043
Serial No.:	10/825,343	Filed:	04/15/2004
Art Unit:	2813	Examiner:	Nguyen, Thanh T.
Customer No.:	23494	Conf. No.:	8658
Title:	Minimizing Transistor Variations Due to Shallow Trench Isolation Stress		

AMENDMENT AFTER FINAL UNDER 37 CFR §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated May 14, 2007. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

Amendments to the Claims are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 4.